

I have invented a processor with a 20-bit address ~~space~~ where the first 640K bytes are RAM, the next 64K bytes are peripherals, and the remaining is flash memory.

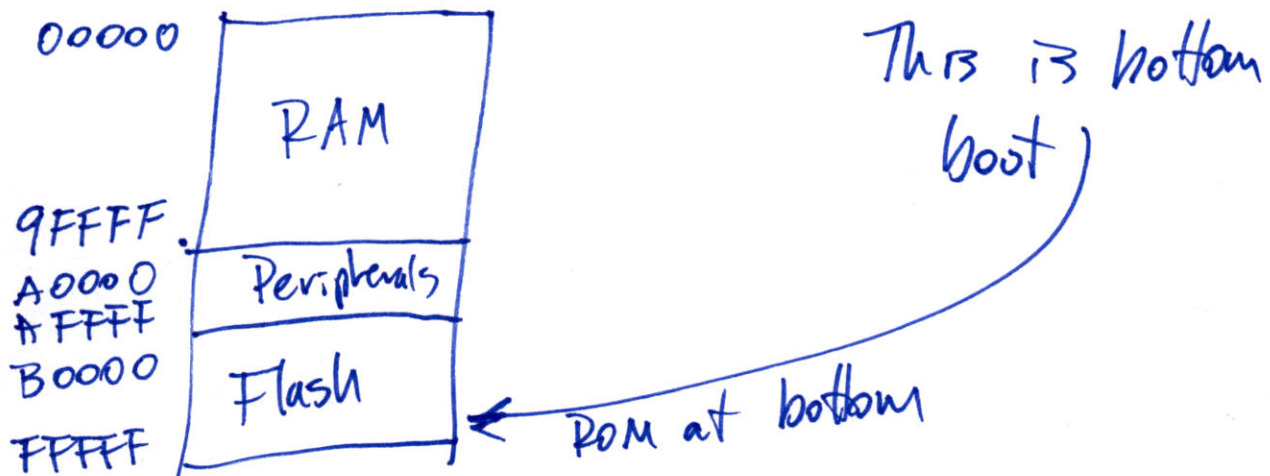
Question 1:
How much flash is there?

Question 2:
Show the memory map of this processor?

Solution

20 bit address = $2^{20} = 1,048,576_{10}$ bytes
= 1024K bytes

Total	1024K
RAM	640K
Periph	64K
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	320K = Flash



I have a very small microcontroller. (2)
It has a 16 bit address space. It starts with 32K of flash, followed by 8K of RAM, followed by 8K of peripheral space. The rest is reserved.
Show the memory map.

Solution

