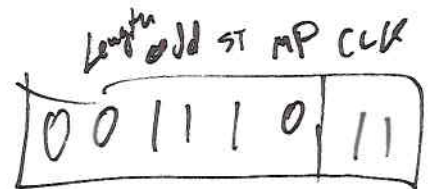


Embedded Systems

10/16/14

8 data bits
7CLK/64
2 stop bits
odd parity
Non multi processor
Async

How do you
set this
communication? ①



SCIO.SMR = 0x3B;
SCIO.SMR.BYTE = 0x3B;

Communicate at 300 bits per second
(later)

SCR Run Asynch interval
SCIO.SCR.BYTE = 0x30;

Write the code to poll the status
register continually until a byte has
been transmitted
while (! (SCIO.SSR.BYTE & 0x04));

Embedded Systems

10/16/14

(2)

Our board, communicate at 300 bps

What do you need to set BRR

$$N = \frac{PCLK}{64 * 2^{2n-1} * B} - 1$$

$N = BRR$, $B = \text{Bit rate}$, $n = \text{CKS setting}$
our board PCLK is 48MHz

$$N = \frac{48000000}{64 * 2^{2*3-1} * 300} - 1$$

$$= 77.125$$

$$= 77$$

($n=3$)

$$= 311$$

($n=2$)

$$= 1249$$

($n=1$)

$$= 4999$$

($n=0$)

It is best to have N as big as possible, but since it is a byte it cannot be more than 255!