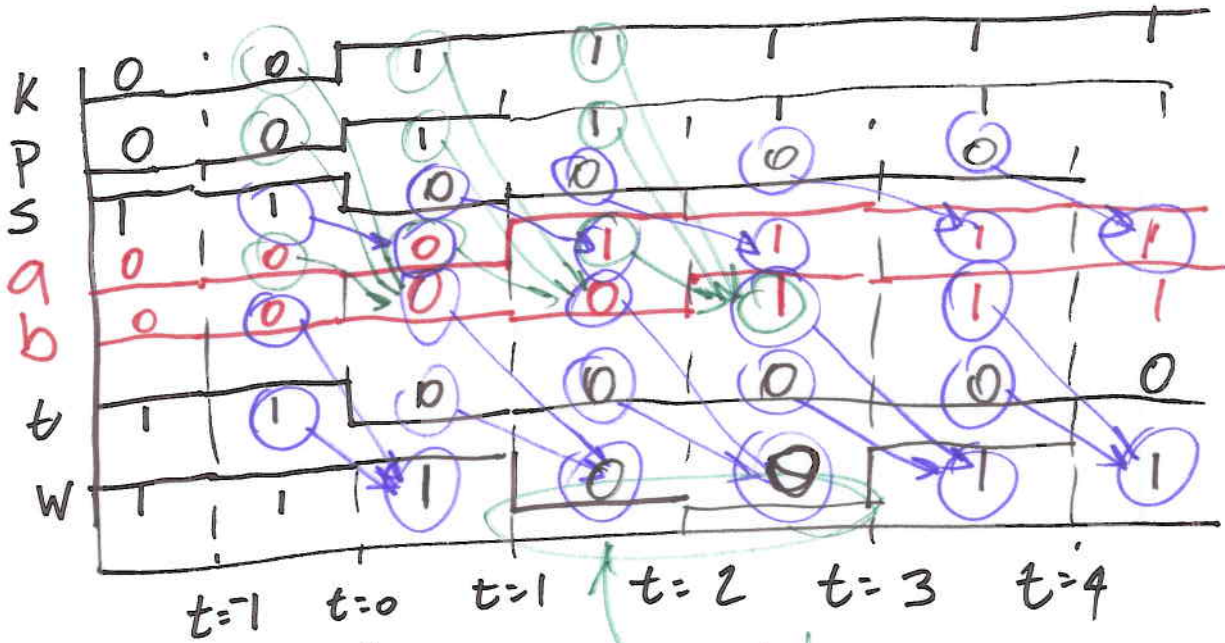
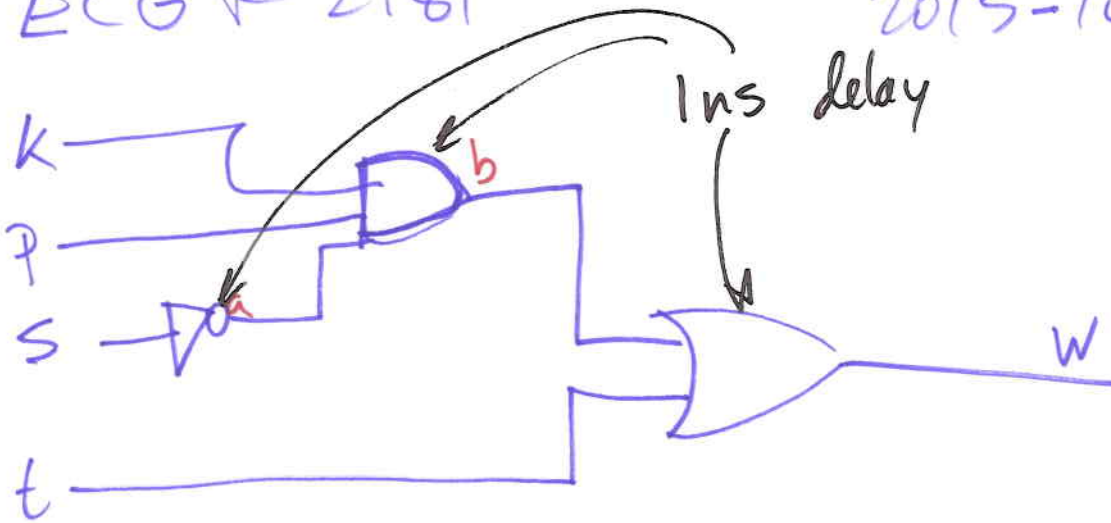


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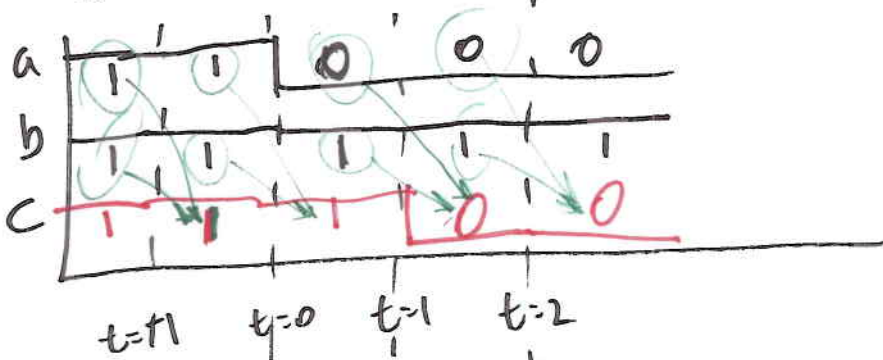
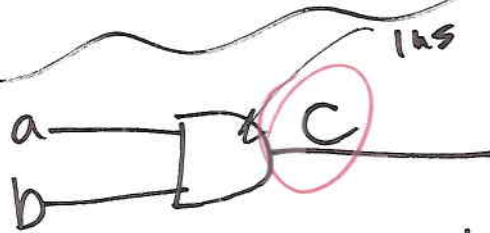
2015-10-20

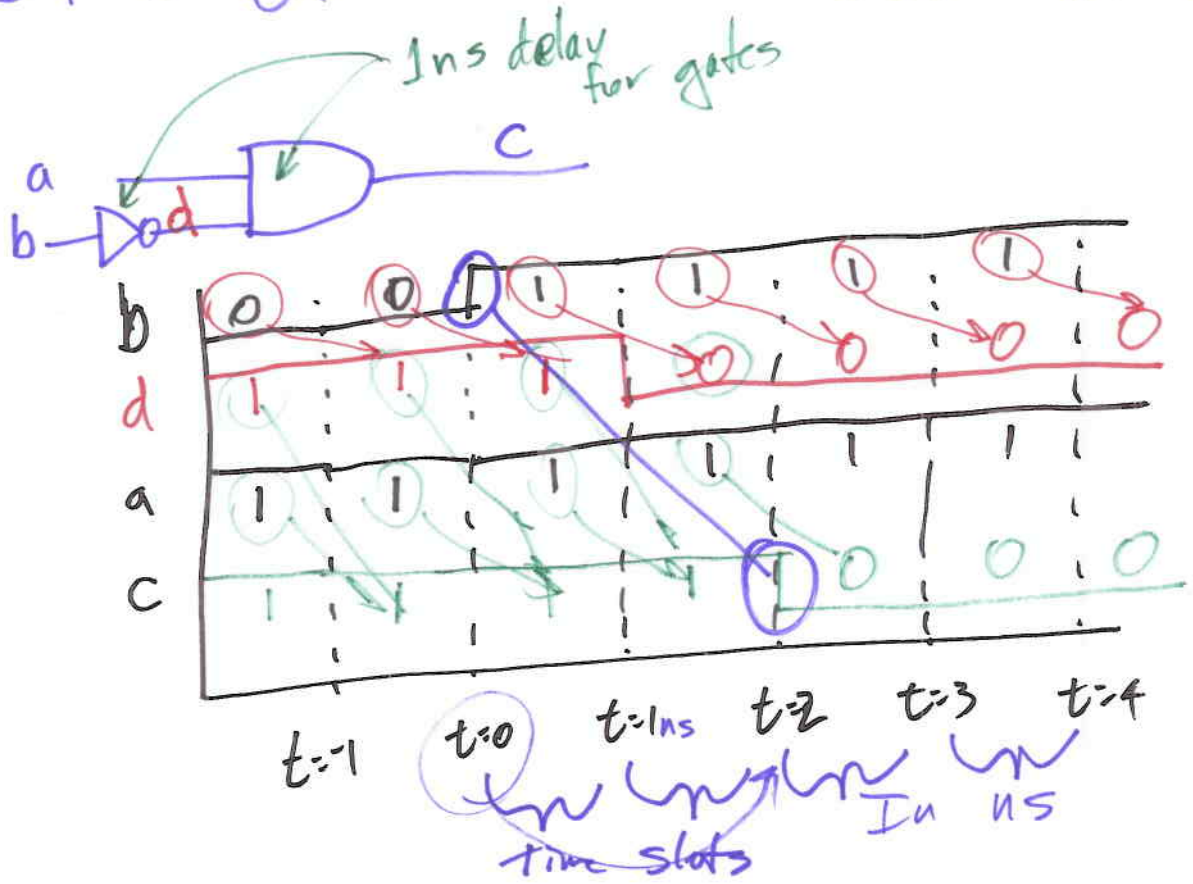
(1)



Changed my inputs

Glitch





Quiz 8: given the logic

~~gates~~ gates above, and a gate delay of 1ns, fill in the timing table. (Done in groups)

Points: for each 4 time slots

*d*: (output of not gate) correct for all time slots (4 points)

*c*: (output of AND gate) correct for all time slots (4 points)

Neatness (2 points)