ECGR 2181 CAD Demonstration Validation Sheet

This sheet should be modified by the student to reflect the current lab assignment being demonstrated

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| Lab Number: | CAD 4 – 8 Bit Signed Accumulator with Seven Segment Display |
| Team Members |

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| Team Member 1: |
| Team Member 2: |

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| Date: |  |

# Lab Demonstration Requirements

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| --- | --- | --- | --- |
| REQ Number | Objective | Self-Review | TAReview |
| 1 | Accumulate positive numbers correctly. |  |  |
| 2 | Accumulate negative numbers correctly. |  |  |
| 3 | Seven segment display shows number correctly. |  |  |
| 4 | The switches are tied to the LEDs. |  |  |

# Code Requirements (will not be graded during lab demo)

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| --- | --- | --- | --- |
| REQ Number | Objective | Self-Review | TAReview |
| 1 | Using structural VHDL, the encoder and ssd\_muxer entities are instantiated. |  |  |
| 2 | top.vhd is the top entity. |  |  |