

**UNCC – ECGR2181- Homework 4**  
**Due 11/09/2015 (Section 001) or 11/10/2015 (Section 002)**

Read Section 3.1 and 3.2

Solve following problems:

- 3.1 – Clock period for clock frequency
- 3.3 – Clock frequency for clock period
- 3.5 – Basic operation of SR latch
- 3.8 – Behavior of level-sensitive SR latch
- 3.10 – Behavior of D latch
- 3.13 – Behavior of edge-triggered D flip-flop
- 3.18 – Design using D flip-flops
- 3.19 – Design using registers