You can preview this quiz, but if this were a real attempt, you would be blocked because:

This quiz is not currently available

**Question 1**

In the following, where are \( x \), \( y \), and \( z \) stored

\[
\text{__global__ mykernel()} \\
\{ \\
\text{int } x, y, z; \\
\text{...} \\
\}
\]

Select one:
- a. In GPU (device) registers if possible.
- b. In GPU (device) global memory
- c. In the CPU (host) registers
- d. In GPU (device) cache memory
- e. None of the other answers

**Question 2**

In GPUs, would a group of threads access a row of consecutive elements in a 2-dimensional array more efficiently than a column of consecutive elements, each thread accessing one element, according to memory coalescing in general?

Select one:
- a. None of the other answers
- b. It makes no difference
- c. Yes
- d. No

**Question 3**

Which CUDA routine is used to synchronize all threads in a multi-block kernel, that is, will wait until all threads in the kernel have completed?

Select one:
- a. CUDABarrier()
- b. None of the other answers
- c. cudaThreadSynchronize()
- d. __threadfence()
- e. __syncthreads()

**Question 4**

What does the CUDA qualifer \text{__device__} indicate when used to prefix an array declaration outside main and any function body?

Select one:
- a. The array will be held in GPU cache memory.
- b. The array will be held in GPU shared memory.
- c. The array will be held in host main memory
- d. The array will be held in GPU global memory.
- e. None of the other answers