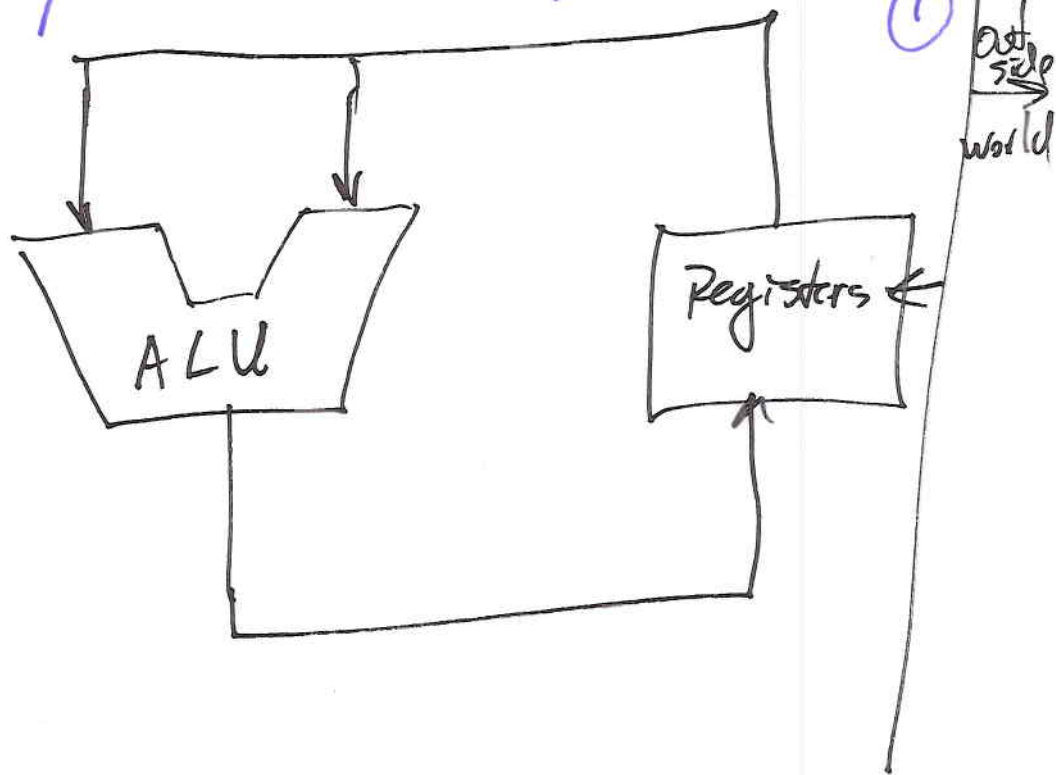


Embedded Systems - Chapter 3



Processor Status word - bits

Overflow $(+) + (+) \rightarrow (-)$

8 bits 2's complement

-128_{10} to 127_{10}

$100_{10} + 100_{10} \rightarrow 200_{10}$

Carry

$(+) + (-) \rightarrow (+)$

Zero

negative

A

B

GT

EQ

LT