## UNC Charlotte-ECGR4101/5101-Midterm Exam -10/05/06

You are permitted 75 minutes to take this test, no more. This is an open book/open notes test. You are allowed the following items for the test: books, notes, homework, labs, pencils and erasers. You are not permitted to have any of the following on your desk during the test: computer, cell phone, calculator or other electronic assistance. Failure to abide by this policy will result in a zero for the test and a visit to the UNC Charlotte honor board. Put your answers on the answer sheet - use only that paper. Turn in the answer sheet at the end of the test. You may keep this test booklet.

Multiple Choice - Questions 1-25: Each of these multiple choice questions is worth 4 points for a correct answer, 0 points for an incorrect answer. Circle your answers on the bubble sheet. CHOOSE THE BEST ANSWER IF TWO OR MORE ANSWERS ARE POSSIBLE.

- 1) According the ECGR4101 programming standards, which of the following should be included in the header of a subroutine?
  - a. Subroutine name

d. Interfaces

b. Assumptions

e. All of the above

main(){

char a = 'x';char b = 'a';

- c. Inputs/Outputs
- 2) MCU can run in three modes: single chip, memory expansion, microprocessor modes. If used in single-chip mode what areas in memory could be addressed?
  - a. Only internal areas (SFR, internal RAM, internal ROM)
  - b. Internal areas (SFR, internal RAM, internal ROM) and external memory areas
  - c. SFR, internal RAM and external memory
  - d. SFR, external memory
  - e. Only external memory
- 3) How is the char b in the code at the right passed to the function choosechar(a,b)?
  - a. Stack

d. R1H

b. R1L

e. R2

c. R1

- f. None of the above
- 4) How is the int d passed in the function dothemath(c,d)?
  - a. Stack

d. R1H

b. R1L

e. R2

c. R1

f. None of the above

- 5) Examine the assembly code at the right. This code corresponds to what C code?
  - a. for(i=0; i<15; i++) x=x+2;
  - b. when(x=0) x=x+2;
  - c. x=0; while (x<16) x=x+2;
  - d. x=0; x=x+2;
  - e. switch(x) case 1: x=x+2;

```
char new;
int d = 5;
float c = 3.14;
float result;

new = choosechar(a,b);
result = dothemath(c,d);
}

mov.w #0000H, -6[FB]
L11:
    cmp.w #0010H, -6[FB]
    jge    L12
    add.w #0002H, -6[FB]
    jmp    L11
```

- 6) What is the number of address bits required to address any byte in a memory that contains 65536 bytes?
  - a. 2
- b. 8
- c. 12
- d. 16

L12:

e. 20

- 7) How many bits does Frame Base register consist of?
  - a. 8
- b. 10
- c. 12
- d. 16
- e. 20

EC	GR 410	01/5101 - N	Iidterm Exam				Page 2 of 4
8)	For ou	r processor	and compiler, ho	ow many bytes c. 3	are required to represe d. 4	ent the data type: char?	
9)	For ou	r processor	and compiler, ho	ow many bytes c. 3	are required to represe d. 4	ent the data type: int? e. 8	
10)	For ou	r processor	and compiler, ho	ow many bytes c. 3	are required to represe d. 4	ent the data type: short ? e. 8	
11)	For ou a. 1	r processor	and compiler, ho b. 2	ow many bytes c. 3	are required to represe d. 4	ent the data type: long? e. 8	
12)	For ou	r processor	and compiler, ho b. 2	w many bytes c. 3	are required to represe d. 4	ent the data type: float ? e. 8	
13)	battery a. 10 b. 20 c. 16 d. No	e drain of <sup>2</sup> /configurate 0.0 hours 0.0 hours 6.6 hours one of the v	400mAh 1.3V ba 4.0 mA at 5.0 to 5 tion how long wor values above above – the config	.5 V. If you hauld the LED st	ad the following	Batteries	To LED
14)	Which a. SP	-	used as a pointer b. PC	to access the a	utomatic variables in d. FP	the current activation receese. AV	ord/stack frame?
15)	a. b.	Larger	and compiler, do	es the stack gr	ow toward larger or si d. Neither e. None of the abo		
16)	ANYT	HING?	s ees ees ytes	ce for our class	s microcontroller used	on our SKP board is not	used for

- e. None of the above
- 17) Which of the following fully describes the outcome of the code at the right?

  a. Add all the numbers from 0 to 100 and put the result in sum.
  - b. Add all the odd numbers from 0 to 100 and put the result in sum.
  - b. Add an the odd numbers from 0 to 100 and put the result in sum.
  - c. Add all the even numbers from 0 to 100 and put the result in sum.
  - d. Add all the odd numbers from N to 100 and put the result in sum.
  - e. Add all the even numbers from N to 100 and put the result in sum.
- 18) What is the CMM (Capability Maturity Model)?
  - a. It is the standard by which engineers can plan product designs.
  - b. This model measures the usefulness of a software program on a scale of 1 to 5.
  - c. The model scales companies by their ability to produce a product in an organized and timely manner.

int i, sum;

for(i=N;i<100;i++){

if((i % 2))

sum += i;

sum = 0;

i = 0;

- d. This model describes how long a piece of software has been around.
- e. It is a model that attempts to project the amount of time a particular product will last before breaking down.

- 19) If we sample input frequency F<sub>i</sub> at a sample rate of F<sub>i</sub> what will the resulting signal look like? b.  $(F_i)^2$ c.  $2 * F_{i}$ d. a horizontal line a. F<sub>i</sub>

e. a vertical line

Match the packaging description below with the labeled picture.

20) DIP (Dual In-Line Package)

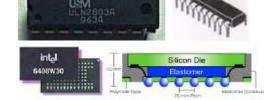
A

21) DFP (Dual Flat Pack)

В

C

- 22) OFP (Quad Flat Pack)
- 23) \_\_\_\_\_ BGA (Ball Grid Array)



- D
- 24) Which of the following packaging types is used in the Furby you examined in lab?
  - a. DIP
- b. DFP
- c. Wirebond
- d. all the above
- e. none of the above

- 25) Which of the following does the sfr62p.h file do?
  - a. Identify the address location of ports and associate them with mnemonic names.
  - b. Initialize the stack pointer
  - c. Set all digital general purpose I/Os to inputs
  - d. Define the location of ROM and RAM
  - e. Set up the variable interrupt vector table

## **Written Answer**

- 26) What area of Flash is available for the user program on the SKP30262 board, assuming the monitor program is still used? List the range of addresses and exact size in bytes (not k bytes). (5 points)
- 27) Given the following information of a particular analog to digital converter, determine the value of the digitally represented voltage and the step size of the converter. (10 points)
  - The device is an 8-bit ADC with a + reference voltage of 5 volts and a reference voltage of -5 volts.
  - The digital representation is: 0011 0010.
- 28) Imagine you have an embedded system that uses your SKP board. The system will:
- Req. 1: Use C programming language
- Req. 2: Set the processor to always read the thermistor value with the Port 10, bit 1, ADC, 8-bits
- Req. 3: Set another ADC to read a light sensor, range +5 to 0 volts, Port 10, bit 2 as an 8-bit number
- Req. 4: Continually poll SW1. While it is pressed, light the red LED.
- Reg. 5: Continually read the thermistor value. If it is above 127 light the yellow LED.
- Req. 6: Continually read the light sensor value. If it is above 127 light the green LED.
- Req. 7: Include a few comments, including headers.

Write the pseudo code which implements the above functionality (25 points)

29) Write the small snippet of code that will set up the light sensor ADC from Problem 28 and start it sampling. Assume that the standard sfr62p.h file is available (10 points). You do not need comments.

## UNC Charlotte-ECGR4101/5101-Midterm Exam -10/05/06

Name: \_\_\_\_\_ Mosaic User ID \_\_\_\_\_

Question	Multiple	Short	Short	Short	Long	Total
	Choice	Answer	Answer	Answer	Answer	
	1-25	26	27	28	29	
Score	/100	/5	/10	/25	/10	/150

Please read and sign this statement: I have not received from anyone nor assisted others while taking this test. I have also notified the test proctor of any of these violations noted above.

Signature:	

Problem 1-25: Answer on the bubble sheet. Enter JUST your name on the sheet (not your ID number, etc).

26.

27.